IN THE SPECIFICATION

Presented below are specification changes showing the changes made.

On page 5, please replace the paragraph that begins on line 20 and ends on page 6 line 15 with the following amended paragraph:

Figure 2 shows an embodiment of multiservice processing system 102. Field processor gate array (FPGA) packet pump 240 sends and receives the PCM data streams to and from the PSTN. Packet pump 240 sends the data streams to and from appropriate digital signal processors (DSP) 250 through HPI bus 270 using a packet pump host port interface (HPI) bus manager 245, which may be implemented as a state machine for example. Each DSP 250 converts PCM data streams received from the packet pump into IP packets, and places the packets onto the IP network using a firmware or software program stored in the DSP internal memory. Each DSP 250 also converts packets received from the IP network into PCM data streams using the stored program, and sends the data streams to packet pump 240. The program may be placed in the DSP internal memory by juke box overlay memory ~~250~~ 260. Juke box overlay memory ~~250~~ 260 stores a library of software programs, and can download the library of software algorithms to the DSPs 250 through the HPI bus. The juke box overlay memory may be a static random access memory (SRAM), for example. The memory 260 is a "juke box" overlay memory because the memory 250 stores several service programs, thus functioning as a "juke box."